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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Cline *et al.*

Examiner: Chen, Eric Brice

Serial No.: 10/711,953

Art Unit: 1765

Filed: 10/15/2004

Docket No.: BUR920040122US1

Title: **DEEP TRENCH FORMATION IN SEMICONDUCTOR DEVICE FABRICATION**

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Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**AMENDEMENT AFTER FINAL REJECTION**

Please enter this amendment that places the claims in conditions for allowance suggested  
by the Examiner.